

**AMENDMENTS TO THE ABSTRACT OF THE DISCLOSURE**

Please amend the Abstract of the Disclosure as follows:

~~Chip design verification apparatus and method. The~~ A method of verifying ~~the chip a~~ chip design includes: a software side operation step of transmitting output data generated by ~~the operation an operation of the software block a software block~~ to the ~~interface means~~ an interface unit, determining whether the ~~output data~~ output data of the hardware block a hardware block received via the interface ~~means~~ unit is valid by executing ~~the chip a chip~~ design verification program, and applying only ~~the valid~~ valid output data of the hardware block to the software block; and a hardware side operation step of transmitting output data generated by ~~the operation an operation of~~ the hardware block to the software block, determining whether the output data of the software block received is valid by executing the chip design verification program in the interface ~~means~~ unit, and applying only ~~the valid~~ valid output data of the software block to the hardware block. ~~Accordingly, chip design errors may be detected when the chip is designed and a faster chip design verification speed may be obtained.~~